

Supplemental Application Data Sheet

Application Information

Application number::	<u>10/594,404</u> Not Yet Assigned
Filing Date::	September 25, 2006
Application Type::	Filing under 35 U.S.C. 371
Subject Matter::	Utility
Title::	TESTING INTEGRATED CIRCUITS
Attorney Docket Number::	016998-004500US
Request for Early Publication::	No
Request for Non-Publication::	No
Total Drawing Sheets::	3
Small Entity?::	No
Petition included?::	No

Applicant Information

Applicant Authority Type::	Inventor
Primary Citizenship Country::	DE
Status::	Full Capacity
Given Name::	Peter
Family Name::	Bergmann
City of Residence::	Erfurt
Country of Residence::	DE
Street of Mailing Address::	c/o Melexis GmbH, Haarbergstrasse 67
City of Mailing Address::	Erfurt
Country of mailing address::	DE
Postal or Zip Code of mailing address::	D-99097

Correspondence Information

Correspondence Customer Number:: 20350

Representative Information

Representative Customer Number:: 20350

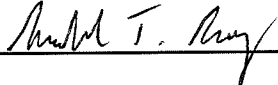
Domestic Priority Information

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	National Stage of Claims Benefit of	PCT/IB05/000755 GB 0406665.0	23 March 2005 25 March 2004

Assignee Information

Assignee Name:: MELEXIS NV
Street of mailing address:: Microelectronic Integrated Systems,
Rozendaalstraat 12
City of mailing address:: Ieper
Country of mailing address:: BE
Postal or Zip Code of mailing address:: B-8900

Submitted by:

Signature  Date January 9, 2009
Printed Name Gerald T. Gray Registration Number 41,797